

L Number	Hits	Search Text	DB	Time stamp
7	192	(memory adj controll\$7) and pld	USPAT	2004/05/16 09:59
8	43	((memory adj controll\$7) and pld) and bidirectiona\$6	USPAT	2004/05/16 09:55
9	2583	(memory adj controll\$7) and bidirectiona\$7	USPAT	2004/05/16 09:59
10	2002	((memory adj controll\$7) and bidirectiona\$7) and logic	USPAT	2004/05/16 09:59
11	441	((memory adj controll\$7) and bidirectiona\$7) and logic) and 710/\$.ccls.	USPAT	2004/05/16 10:00
12	197	((memory adj controll\$7) and bidirectiona\$7) and logic) and 710/\$.ccls.) and ((memory) adj (module41 or device\$1))	USPAT	2004/05/16 10:00
13	255	((memory adj controll\$7) and bidirectiona\$7) and logic) and 710/\$.ccls.) and ((memory) adj (module\$1 or device\$1))	USPAT	2004/05/16 11:49
14	8	("4513374") or ("6502161") or ("6378018") or ("6449213") or ("6530006") or ("6215727") or ("6493250") or ("6373302")).PN.	USPAT	2004/05/16 11:51
-	1065186	interrup\$4 near\$4 overload\$5	USPAT	2003/04/23 18:15
-	61404	(interrup\$4 near\$4 overload\$5) and vector	USPAT	2003/04/23 17:37
-	32023	((interrup\$4 near\$4 overload\$5) and vector) and table	USPAT	2003/04/23 17:37
-	525	((interrup\$4 near\$4 overload\$5) and vector) and table) and dsp?	USPAT	2003/04/23 17:38
-	19	((interrup\$4 near\$4 overload\$5) and vector) and table) and dsp?) and mpeg?	USPAT	2003/04/23 17:38
-	200	interrup\$4 near overload\$5	USPAT	2003/04/23 17:40
-	0	(interrup\$4 near overload\$5) and mpeg?	USPAT	2003/04/23 17:40
-	0	(interrup\$4 near overload\$5) and dsp?	USPAT	2003/04/23 17:41
-	9	(interrup\$4 near overload\$5) and priorit\$4	USPAT	2003/04/23 17:44
-	6	(interrup\$4 near overload\$5) and vector	USPAT	2003/04/23 17:45
-	84	table adj vector?	USPAT	2003/04/23 17:45
-	15	(table adj vector?) and (overload\$5 or overrid\$4)	USPAT	2003/04/23 17:52
-	97	overload\$4 adj interrup\$5	USPAT	2003/04/23 17:54
-	16	(overload\$4 adj interrup\$5) and (vector? or pin?)	USPAT	2003/04/23 17:53
-	1	(overload\$4 adj interrup\$5) and 710/\$.ccls.	USPAT	2003/04/23 17:56
-	15519	(static) adj ((ram) or (random))	USPAT	2003/04/23 17:57
-	1916	((static) adj ((ram) or (random))) and event? and address\$4	USPAT	2003/04/23 17:58
-	866	((static) adj ((ram) or (random))) and event? and address\$4) and (vector? or table?)	USPAT	2003/04/23 17:58
-	690	((static) adj ((ram) or (random))) and event? and address\$4) and (vector? or table?) and interrup\$6	USPAT	2003/04/23 17:58
-	76	((static) adj ((ram) or (random))) and event? and address\$4) and (vector? or table?) and interrup\$6) and (overload\$5 or overrid\$5)	USPAT	2003/04/23 17:59
-	500	((static) adj ((ram) or (random))) and vector? and pins	USPAT	2003/04/23 18:15

-	269	((static) adj ((ram) or (random))) and vector? and pins) and (event? or adress\$4)	USPAT	2003/04/23 18:17
-	229	((static) adj ((ram) or (random))) and vector? and pins) and (event? or adress\$4)) and interrup\$6	USPAT	2003/04/23 18:19
-	197	((static) adj ((ram) or (random))) and vector? and pins) and (event? or adress\$4)) and interrup\$6) and ((gpi?) or (general adj purpose))	USPAT	2003/04/23 18:20
-	144	((static) adj ((ram) or (random))) and vector? and pins) and (event? or adress\$4)) and interrup\$6) and ((gpi?) or (general adj purpose)) and ((dsp?) or (digital adj signal))	USPAT	2003/04/23 18:25
-	123	((static) adj ((ram) or (random))) and vector? and pins) and (event? or adress\$4)) and interrup\$6) and ((gpi?) or (general adj purpose)) and ((dsp?) or (digital adj signal)) and (overload\$ or overrid\$6)	USPAT	2003/04/23 18:35
-	3590	external adj bus	USPAT	2003/05/30 20:33
-	106	(external adj bus) and count\$4 and (wait\$4 adj time)	USPAT	2003/05/30 20:34
-	55	((external adj bus) and count\$4 and (wait\$4 adj time)) and ((dma) or (direct adj memory))	USPAT	2003/05/30 20:49
-	152	(external adj bus) and lsi and ((dma) or (direct adj memory))	USPAT	2003/05/30 20:49
-	143	((external adj bus) and lsi and ((dma) or (direct adj memory))) and count\$5	USPAT	2003/05/30 21:37
-	182	pcmcia and ((sd) or (secure adj digital))	USPAT	2003/05/30 21:40
-	117	(pcmcia and ((sd) or (secure adj digital))) and peripheral\$1	USPAT	2003/05/30 21:40
-	90	((pcmcia and ((sd) or (secure adj digital))) and peripheral\$1) and (audio or imag\$5)	USPAT	2003/05/30 21:46
-	1	("5918163").PN.	USPAT	2003/05/30 21:46
-	16	("4849765" "4920352" "5004899" "5138328" "5183404" "5300938" "5337061" "5343319" "5357091" "5361061" "5365221" "5373149" "5410749" "5412392" "5519577" "5563400").PN.	USPAT	2003/05/30 21:47
-	22	5918163.URPN.	USPAT	2003/05/30 22:00
-	498	prefetch\$5 and pci and cycle\$1 and master	USPAT	2003/05/30 22:03
-	44	(prefetch\$5 and pci and cycle\$1 and master) and ((cycle) adj (end\$4 or terminat\$4))	USPAT	2003/05/30 22:39
-	251	(prefetch\$5 and pci and cycle\$1 and master) and cycle\$1 and fifo	USPAT	2003/05/30 22:40
-	241	((prefetch\$5 and pci and cycle\$1 and master) and cycle\$1 and fifo) and logic	USPAT	2003/05/30 22:40
-	240	((prefetch\$5 and pci and cycle\$1 and master) and cycle\$1 and fifo) and logic) and buffer	USPAT	2003/05/30 22:41
-	9	((prefetch\$5 and pci and cycle\$1 and master) and cycle\$1 and fifo) and logic) and buffer) and ((stor\$4 or retain\$4) adj (prefetch))	USPAT	2003/05/30 22:56
-	0	pre adj fech\$5	USPAT	2003/05/30 22:57
-	1214	pre adj fetch\$5	USPAT	2003/05/30 22:57

-	13	(pre adj fetch\$5) and (cycle adj termination)	USPAT	2003/05/30 22:59
-	930	(pre adj fetch\$5) and buffer	USPAT	2003/05/30 23:00
-	42	((pre adj fetch\$5) and buffer) and (pci adj master)	USPAT	2003/05/30 23:01
-	359	serial\$7 and (elastic adj buffer\$6)	USPAT	2004/05/10 10:49
-	269	(serial\$7 and (elastic adj buffer\$6)) and memory	USPAT	2004/05/10 10:50
-	52	((serial\$7 and (elastic adj buffer\$6)) and memory) and (phase adj align\$7)	USPAT	2004/05/10 10:56
-	738	serial adj memory	USPAT	2004/05/10 10:56
-	67	(serial adj memory) and ((single) adj (chip or ic))	USPAT	2004/05/10 11:06
-	299	(serial adj memory) and phase	USPAT	2004/05/10 11:06
-	184	((serial adj memory) and phase) and buffer\$6	USPAT	2004/05/10 12:49
-	1	("6594325").PN.	USPAT	2004/05/10 12:52
-	0	("serialadjreadadjdataadjport\$1").PN.	USPAT	2004/05/10 12:52
-	0	serial adj read adj data adj port41	USPAT	2004/05/10 12:53
-	0	read adj data adj port41	USPAT	2004/05/10 12:53
-	1	serial adj read adj data adj port\$1	USPAT	2004/05/10 12:53
-	194	read adj data adj port\$1	USPAT	2004/05/10 12:53
-	120	(read adj data adj port\$1) and synchroni\$6	USPAT	2004/05/10 12:54
-	117	((read adj data adj port\$1) and synchroni\$6) and memory	USPAT	2004/05/10 12:56
-	99	(((read adj data adj port\$1) and synchroni\$6) and memory) and serial\$7	USPAT	2004/05/10 12:56
-	92	((((read adj data adj port\$1) and synchroni\$6) and memory) and serial\$7) and buffer\$1	USPAT	2004/05/10 13:11
-	6542	memory adj interface	USPAT	2004/05/10 13:11
-	3073	(memory adj interface) and (high adj speed)	USPAT	2004/05/10 13:11
-	425	((memory adj interface) and (high adj speed)) and (low adj speed)	USPAT	2004/05/10 13:11
-	279	(((memory adj interface) and (high adj speed)) and (low adj speed)) and serial\$8	USPAT	2004/05/10 13:17
-	92	((((memory adj interface) and (high adj speed)) and (low adj speed)) and serial\$8) and (710/\$.ccls. or 711/\$.ccls. or 365/\$.ccls.)	USPAT	2004/05/10 17:07

-	148	("3585378" "3651473" "3761879" "3803562" "3810301" "3835530" "3849872" "3859640" "3983537" "4007452" "4038648" "4063225" "4071887" "4092733" "4132869" "4188670" "4215430" "4227045" "4236087" "4319356" "4329685" "4355387" "4379327" "4400794" "4407014" "4414480" "4438352" "4458297" "4467400" "4494196" "4605928" "4612613" "4615017" "4627058" "4630355" "4637073" "4639861" "4639933" "4644502" "4646298" "4649384" "4653050" "4663758" "4667328" "4680780" "4703436" "4707808" "4719621" "4736365" "4748588" "4769789" "4782457" "4785415" "4823363" "4839909" "4847615" "4855613" "4860285" "4864496" "4866508" "4872137" "4876700" "4881232" "4884122" "4890224" "4897818" "4906987" "4907062" "4912633" "4926382" "4937203" "4943914" "4943966" "4954854" "4955020" "4961169" "4970724" "4974048" "4980765" "4984192" "4985895" "5001712" "5003558" "5008882" "5020020" "5021985" "5043820" "5045725" "5051938" "5055897" "5077596" "5077737" "5077738" "5103424" "5111271" "5111434" "5118975" "5125006" "5128737" "5131015" "5133064" "5145645" "5159273" "5161152" "5168331" "5179687" "5187779" "5204836" "5206832" "5214657" "5218686" "5226147" "5227677" "5243573" "5243623" "5243703" "5247522" "5249282" "5252507" "5254883" "5257235" "5261077" "5262986" "5265216" "5267200" "5268639" "5278967" "5297080" "5297092" "5301152" "5311083" "5317540" "5325329" "5329174" "5329559" "5359722" "5371420" "5379258" "5402388" "5420824" "5434996" "5465223" "5498886" "5561630" "5699317" "5721862" "5887272" "6483755").PN.	USPAT	2004/05/10 13:19
-	4	("5392407" "5787298" "5859809" "5938742").PN.	USPAT	2004/05/10 13:30
-	16	("4718080" "4775931" "4991085" "5127041" "5134648" "5167021" "5181858" "5249218" "5302947" "5333177" "5357625" "5384890" "5410667" "5428671" "5430793" "5649001").PN.	USPAT	2004/05/10 13:36
-	5	("3394354" "3408637" "3723984" "3964030" "4069970").PN.	USPAT	2004/05/10 13:43
-	53	((((memory adj interface) and (high adj speed)) and (low adj speed)) and serial\$8) and (memory adj array)	USPAT	2004/05/10 17:20
-	70	((((memory adj interface) and (high adj speed)) and (low adj speed)) and serial\$8) and (memory adj modules)	USPAT	2004/05/10 17:27
-	379	(memory adj interface) and synchroni\$7 and (memory adj modules)	USPAT	2004/05/10 17:27
-	353	((memory adj interface) and synchroni\$7 and (memory adj modules)) and buffer\$1	USPAT	2004/05/10 17:27

-	157	((memory adj interface) and synchroni\$7 and (memory adj modules) and buffer\$1) and (710/\$.ccls. or 711/\$.ccls. or 365/\$.ccls.)	USPAT	2004/05/10 18:31
-	10	("5926046" "5964880" "6014042" "6016282" "6047346" "6125157" "6128025" "6185644" "6295593" "6333959").PN.	USPAT	2004/05/10 17:40
-	3	("5919254" "6128700" "6172937").PN.	USPAT	2004/05/10 17:49
-	1	"5933614".PN.	USPAT	2004/05/10 17:55
-	2	((("6594325") or ("6373302"))).PN.	USPAT	2004/05/11 14:33
-	0	"6530006" and encod\$6	USPAT	2004/05/11 14:33
-	0	"6530006" and decod\$7	USPAT	2004/05/14 17:19
-	17451	((programmable adj logic) or (pld))	USPAT	2004/05/14 17:19
-	2860	((((programmable adj logic) or (pld))) and ((bi adj directional) or (bidirectional)))	USPAT	2004/05/14 17:21
-	1867	((((programmable adj logic) or (pld))) and ((bi adj directional) or (bidirectional))) and ((controller) or (mocrecontroller))	USPAT	2004/05/14 17:21
-	1801	(((((programmable adj logic) or (pld))) and ((bi adj directional) or (bidirectional))) and ((controller) or (mocrecontroller))) and (state\$1 or mode\$1)	USPAT	2004/05/14 17:23
-	246	(((((programmable adj logic) or (pld))) and ((bi adj directional) or (bidirectional))) and ((controller) or (mocrecontroller))) and (state\$1 or mode\$1) and (710/\$.ccls. or 367/\$.ccls.)	USPAT	2004/05/14 17:49
-	316	(((((programmable adj logic) or (pld))) and ((bi adj directional) or (bidirectional))) and ((controller) or (mocrecontroller))) and (state\$1 or mode\$1) and ((two) adj (wire\$1 or line\$1))	USPAT	2004/05/14 17:58
-	136	(((((programmable adj logic) or (pld))) and ((bi adj directional) or (bidirectional))) and ((controller) or (mocrecontroller))) and (state\$1 or mode\$1) and ((two) adj (wire\$1 or line\$1)) and (radio or vehicle\$1)	USPAT	2004/05/14 17:53
-	118	bus adj ((two) adj (wire\$1 or line\$1))	USPAT	2004/05/14 18:07
-	1195	((((programmable adj logic) or (pld))) and ((two) adj (wire\$1 or line\$1)) or (inter adj integrated))	USPAT	2004/05/14 18:10
-	84	(((((programmable adj logic) or (pld))) and ((two) adj (wire\$1 or line\$1)) or (inter adj integrated))) and 710/\$.ccls.	USPAT	2004/05/14 18:18
-	72	((((programmable adj logic) or (pld))) and ((two) adj (wire\$1 or line\$1)) or (inter adj integrated))) and collision\$1	USPAT	2004/05/14 18:22
-	17869	sleep	USPAT	2004/05/14 18:22
-	33	(((((programmable adj logic) or (pld))) and ((two) adj (wire\$1 or line\$1)) or (inter adj integrated))) and sleep	USPAT	2004/05/14 18:24

-	384	(((programmable adj logic) or (pld))) and (((two) adj (wire\$1 or line\$1)) or (inter adj integrated))) and bidirectional	USPAT	2004/05/14 18:24
-	384	(((programmable adj logic) or (pld))) and (((two) adj (wire\$1 or line\$1)) or (inter adj integrated))) and bidirectional) and (logic circuit\$1)	USPAT	2004/05/16 09:40
-	7	("Re34363" "4713557" "4758985" "4835418" "4987319" "5705937" "5736870").PN.	USPAT	2004/05/14 18:32